

WHAT IS CLAIMED IS:

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- 5 1. A liquid crystal display device comprising:
- (a) a pixel electrode;
  - (b) a first signal line extending along one side of said pixel electrode; and
  - (c) a second signal line extending along the other side of said pixel electrode,
- a first length along which said pixel electrode and said first signal line are adjacent to each other being equal to a second length along which said pixel electrode and said second signal line are adjacent to each other,
- 10 a first space between said pixel electrode and said first signal line being equal to a second space between said pixel electrode and said second signal line.
- 15 2. The liquid crystal display device as set forth in claim 1, wherein said pixel electrode includes a region having been exposed to light, at a periphery thereof.
3. The liquid crystal display device as set forth in claim 1, wherein said pixel electrode is formed in a layer in which said first and second signal lines are also formed.
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4. The liquid crystal display device as set forth in claim 1, wherein said first and second signal lines are formed in a first layer, and said pixel electrode is formed in a second layer which is electrically isolated from said first layer by an insulating layer sandwiched between said first and second layers.
- 25 5. A liquid crystal display device comprising:
- (a) a pixel electrode;
  - (b) a first signal line extending along one side of said pixel electrode; and
  - (c) a second signal line extending along the other side of said pixel electrode,
- at least one of said first and second signal lines having a projecting portion

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extending towards said pixel electrode,

a first length along which said pixel electrode and said first signal line are adjacent to each other being equal to a second length along which said pixel electrode and said second signal line are adjacent to each other,

5 a first space between said pixel electrode and said first signal line being equal to a second space between said pixel electrode and said second signal line.

6. The liquid crystal display device as set forth in claim 5, wherein said pixel electrode includes a region <sup>or</sup> having been exposed to light, at a periphery thereof.

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a7 7. The liquid crystal display device as set forth in claim 5, wherein said pixel electrode is formed in a layer in which said first and second signal lines are also formed.

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8. The liquid crystal display device as set forth in claim 5, wherein said first and second signal lines are formed in a first layer, and said pixel electrode is formed in a second layer which is electrically isolated from said first layer by an insulating layer sandwiched between said first and second layers.

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9. A liquid crystal display device comprising:

(a) a pixel electrode;

(b) a thin film transistor located adjacent to said pixel electrode and at one side of said pixel electrode;

(c) a first signal line extending along said one side of said pixel electrode; and

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(d) a second signal line extending along the other side of said pixel electrode,

Fig. 11  
said pixel electrode being formed with a cut-out portion at the other side thereof, said cut-out portion having a length equal to a length along which said first signal line cannot be adjacent to said pixel electrode by said thin film transistor,

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a first length along which said pixel electrode and said first signal line are adjacent to each other being equal to a second length along which said pixel electrode and said second signal line are adjacent to each other,

5 a first space between said pixel electrode and said first signal line being equal to a second space between said pixel electrode and said second signal line.

10. The liquid crystal display device as set forth in claim 9, wherein said pixel electrode includes a region having been exposed to light, at a periphery thereof.

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11. The liquid crystal display device as set forth in claim 9, wherein said pixel electrode is formed in a layer in which said first and second signal lines are also formed.

15 12. The liquid crystal display device as set forth in claim 9, wherein said first and second signal lines are formed in a first layer, and said pixel electrode is formed in a second layer which is electrically isolated from said first layer by an insulating layer sandwiched between said first and second layers.

20 13. A liquid crystal display device comprising:

(a) a plurality of pixel electrodes each spaced away from adjacent ones by a predetermined distance;

Fig. 12

(b) first signal lines each extending along one side of each of said pixel electrodes, each of said first signal lines having a bending portion which extends along a periphery of each of said pixel electrodes; and

25 (c) second signal lines each extending along the other side of each of said pixel electrodes, each of said second signal lines having a bending portion which extends along a periphery of each of said pixel electrodes,

at least one of said first and second signal lines having a projecting portion

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extending towards said pixel electrode,

a first length along which said pixel electrode and said first signal line are adjacent to each other being equal to a second length along which said pixel electrode and said second signal line are adjacent to each other,

5 a first space between said pixel electrode and said first signal line being equal to a second space between said pixel electrode and said second signal line.

14. The liquid crystal display device as set forth in claim 13, wherein each of said pixel electrodes include a region having been exposed to light, at a periphery thereof.

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15. The liquid crystal display device as set forth in claim 13, wherein each of said pixel electrodes is formed in a layer in which said first and second signal lines are also formed.

16. The liquid crystal display device as set forth in claim 13, wherein said first and second signal lines are formed in a first layer, and said pixel electrodes are formed in a second layer which is electrically isolated from said first layer by an insulating layer sandwiched between said first and second layers.

17. A method of fabricating a liquid crystal display device including a pixel electrode, a first signal line extending along one side of said pixel electrode, and a second signal line extending along the other side of said pixel line, comprising the steps of:

25 (a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;

(b) forming a channel on said gate insulating film above said scanning line;

(c) forming said first and second signal lines so that a first length along which said pixel electrode and said first signal line are adjacent to each other is

equal to a second length along which said pixel electrode and said second signal line are adjacent to each other, and a first space between said pixel electrode and said first signal line is equal to a second space between said pixel electrode and said second signal line,

- 5 (d) forming said pixel electrode; and  
(e) covering a product resulting from said step (d) with an insulating film.

18. The method as set forth in claim 17, wherein said pixel electrode is formed between said first and second signal lines in a common layer in said step

10 (d).

19. The method as set forth in claim 18, wherein said pixel electrode is formed, after said insulating film has been formed, on said insulating film above a region sandwiched between said first and second signal lines.

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20. A method of fabricating a liquid crystal display device including a pixel electrode, a first signal line extending along one side of said pixel electrode, and a second signal line extending along the other side of said pixel line, comprising the steps of:

- 20 (a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;  
(b) forming a channel on said gate insulating film above said scanning line;  
(c) forming said first and second signal lines so that at least one of said first and second signal lines has a projecting portion extending towards said pixel electrode and that a first length along which said pixel electrode and said first signal line are adjacent to each other is equal to a second length along which said pixel electrode and said second signal line are adjacent to each other, and a first space between said pixel electrode and said first signal line is equal to a second space between said pixel electrode and said second signal line,
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(d) forming said pixel electrode; and

(e) covering a product resulting from said step (d) with an insulating film.

21. The method as set forth in claim 20, wherein said pixel electrode is  
5 formed between said first and second signal lines in a common layer in said step  
(d).

22. The method as set forth in claim 20, wherein said pixel electrode is  
formed, after said insulating film has been formed, on said insulating film above a  
10 region sandwiched between said first and second signal lines.

23. A method of fabricating a liquid crystal display device including a pixel  
electrode, a first signal line extending along one side of said pixel electrode, and a  
second signal line extending along the other side of said pixel line, comprising the  
15 steps of:

(a) forming a scanning line on a transparent substrate, and then, forming a  
gate insulating film on said scanning line and said transparent substrate;

(b) forming a channel on said gate insulating film above said scanning line;

20 (c) forming said first and second signal lines so that at least one of said first  
and second signal lines has a cut-out portion having a length equal to a length  
along which said first and/or second signal line(s) cannot be adjacent to said pixel  
electrode by a thin film transistor formed at one side of said pixel electrode,

said cut-put portion being formed at the other side of said pixel electrode,  
a first length along which said pixel electrode and said first signal line are  
25 adjacent to each other being equal to a second length along which said pixel  
electrode and said second signal line are adjacent to each other,

a first space between said pixel electrode and said first signal line being  
equal to a second space between said pixel electrode and said second signal line,

(d) forming said pixel electrode; and

(e) covering a product resulting from said step (d) with an insulating film.

24. The method as set forth in claim 23, wherein said pixel electrode is formed between said first and second signal lines in a common layer in said step (d).

25. The method as set forth in claim 23, wherein said pixel electrode is formed, after said insulating film has been formed, on said insulating film above a region sandwiched between said first and second signal lines.

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26. A method of fabricating a liquid crystal display device including a plurality of pixel electrodes each spaced away from adjacent ones by a predetermined distance, first signal lines each extending along one side of each of said pixel electrodes, each of said first signal lines having a bending portion which extends along a periphery of each of said pixel electrodes, and second signal lines each extending along the other side of each of said pixel electrodes, each of said second signal lines having a bending portion which extends along a periphery of each of said pixel electrodes, comprising the steps of:

(a) forming a scanning line on a transparent substrate, and then, forming a gate insulating film on said scanning line and said transparent substrate;

(b) forming a channel on said gate insulating film above said scanning line;

(c) forming said first and second signal lines so that at least one of each of said first signal lines and each of second signal lines has a projecting portion extending towards each of said pixel electrodes and that a first length along which each of said pixel electrodes and each of said first signal lines are adjacent to each other is equal to a second length along which each of said pixel electrodes and each of said second signal lines are adjacent to each other, and a first space between each of said pixel electrodes and each of said first signal lines is equal to a second space between each of said pixel electrodes and each of said second signal

lines.

(d) forming said pixel electrodes; and

(e) covering a product resulting from said step (d) with an insulating film.

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27. The method as set forth in claim 26, wherein each of said pixel electrodes is formed between each of said first signal lines and each of said second signal lines in a common layer in said step (d).

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28. The method as set forth in claim 26, wherein each of said pixel electrodes is formed, after said insulating film has been formed, on said insulating film above a region sandwiched between said first and second signal lines.